

Efficient circuit-level analysis of large microwave systems by Krylov-subspace harmonic balance

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The paper proposes a rigorous harmonic balance technique for the circuit-level simulation of complex microwave systems consisting of many interconnected functional blocks. The voltages at the interface ports between building blocks are used as auxiliary unknowns, and are determined simultaneously with the block state variables by a Krylov-subspace inexact Newton iteration. This provides exact results and allows large savings of both memory and CPU time.

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